**Amendments to the Drawings:** 

The attached sheet of drawings includes changes to sheet 10/10. This sheet which

includes Figure 10A, Figure 10B, and Figure 10C, replaces the original sheet including

Figure 10A, Figure 10B, and Figure 10C. In sheet 10/10 previously omitted notation

"New Sheet" has been added.

Attachment:

**New Sheet** 

**Annotated Sheet Showing Changes** 

## **REMARKS/ARGUMENTS**

Applicant respectfully requests reconsideration of this application in view of the following remarks.

## **NON-COMPLIANT AMENDMENT (37 CFR 1.121)**

Applicants submit that as amended sheet 10/10 complies with 37 C.F.R. § 1.121(d), because notation "New Sheet" has been added.

PO to Addressee: ED 882964014 US

## **CONCLUSION**

Applicants respectfully submit that sheet 10/10 is in condition for examination, and requests examination.

The Examiner is invited to call Alan Heimlich at 408 253-3860 if there remains any issue with examination. Cleartext email communication is authorized.

Respectfully submitted,

Alan Heimlich / Reg 48808

Attorney for Applicants

Digitally signed by Alan Heimlich

Heimlich Law

10/01/2005

Date

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Title: Method and Apparatus for an Output Buffer with Dynamic Impedance Control
First named Inventor: Duncan McRae - Tel: 408 253 3860
Application Number: 10/765370 - Confirmation Number: 1576
Express Mail: ED 882964014 US - Filing Date: 01/27/2004 - Docket No.: 100303.P1855
Amendment dated: 10/01/2005 Reply to NOTICE OF NON-COMPLIANT AMENDMENT (37 CFR 1.121) of 09/20/2005

Annotated Sheet Showing Changes

New Sheet added

output signal to at least two or more transistors transistor array having based on the received Receive the generated Generate an output Feed back a signal one transistor in a stacked output output signal signal 1004 1002 output signal through a two or more transistors output signal to at least based on the received Receive the generated transistor array having Generate an output one transistor in a Feed back a signal after passing the stacked output output signal signal 1014 1012 output signal to at least two or more transistors transistor array having based on the received Receive the generated after comparing the one transistor in a Feed back a signal Generate an output stacked output output signal signal 1024 1022

FIG. 10B

a n type transistor, and a resistor, a capacitor,

signal through a device selected from the

and passing the output

to a reference voltage received output signa

n type transistor, and a

p type transistor.

resistor, a capacitor, a

group consisting of a

a p type transistor.

the group consisting of

device selected from

FIG. 10A

FIG. 10C